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Application No. 10/735,904
Amendment due November 1, 2006
Reply to Office Action of August 1, 2006

Docket No.: 2019-0232P

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A video driving module for ~~multiple~~ driving a plurality of monitors, comprising:

a CRT controller which receives a first video signal having a first color data and
converting ~~converts~~ a plurality of second video signals having second color data from the first
video signal; and

a plurality of converters;

wherein a size of the second color data is less than a size of the first color data, and the
second video signals has the same second color data ~~the CRT controller generates a video signal~~
~~and the video signal is divided into a plurality of equal parts, each of the parts being associated~~
~~with one of the converters.~~

2. (Original) The video driving module as in claim 1, wherein the CRT controller converts a plurality of parts of the image signal into the plurality of video signals.

3. (Original) The video driving module as in claim 2, further comprising a video memory to store the image signal.

4. (Cancelled)

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5. (Original) The video driving module as in claim 1, wherein the CRT controller has a graphics engine.

6. (Original) The video driving module as in claim 1, wherein the CRT controller has generates a vertical/horizontal sync signal to the monitors.

7. (Original) The video driving module as in claim 1, wherein the converter is a digital-to-analog converter (DAC).

8. (Currently Amended) A motherboard for driving a plurality of multiple monitors, comprising:

a chipset for outputting a plurality of image signals;

a CRT controller for converting the plurality of image signals into a plurality of digital video signals and outputting a vertical/horizontal synchronization signal to the monitors, wherein each of the digital video signals has the same digital video; and

a plurality of digital-to-analog converters for converting the digital video signals into a plurality of analog video signals, adapted for the monitors and outputting the analog video signals to the monitors.

9-10. (Cancelled)

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11. (Original) The motherboard as in claim 8, further comprising a video memory to store the image signal.

12. (Currently Amended) A method for driving ~~multiple~~ a plurality of monitors, ~~a the~~ plurality of monitors being driven by a CRT controller and a plurality of converters, ~~the method~~ comprising the following steps:

converting the CRT controller processing a plurality of image signals into a plurality of digital video signals via the CRT controller, and outputting a vertical/horizontal synchronization signal to the monitors, wherein each of the digital video signals has the same digital video;

sending transmitting the plurality of digital video signals to the plurality of converters for converting the digital video signals into a plurality of analog signals adapted for the monitors; and

transmitting sending the plurality of analog signals adapted for outputting to the monitors to the monitors.

13. (Currently Amended) The method for driving multiple monitors as in claim 12, further comprising ~~a step of~~ storing the image signals in a video memory.

14-15. (Cancelled)

16. (New) The video driving module as in claim 1, wherein the size of the second color data multiplies a number of the second video signals is equal the size of the first color data.